

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
-60V	25mΩ @ V <sub>GS</sub> = -10V	-7.7A
	33mΩ @ V <sub>GS</sub> = -4.5V	-6.8A

## Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Backlighting
- Power-management functions
- DC-DC converters

## Features and Benefits

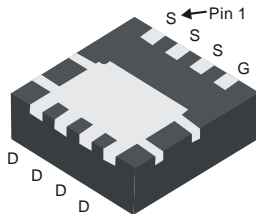
- Low R<sub>DS(ON)</sub> – Ensures on State Losses Are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **The DMP6023LFGQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

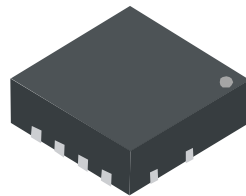
## Mechanical Data

- Package: PowerDI<sup>®</sup>3333-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.034 grams (Approximate)

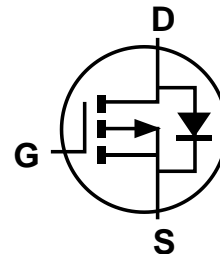
PowerDI3333-8



Bottom View



Top View



Equivalent Circuit

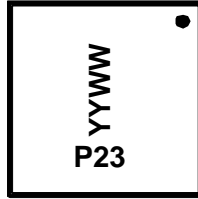
## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP6023LFGQ-7	PowerDI3333-8	2,000	Tape & Reel
DMP6023LFGQ-13	PowerDI3333-8	3,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

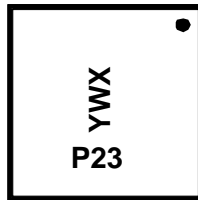
## Marking Information

Site1:



P23 = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Last Two Digits of Year (ex: 23 = 2023)  
 WW = Week Code (01 to 53)

Site2:



P23 = Product Type Marking Code  
 YWX = Date Code Marking  
 Y = Year (ex: 3 = 2023)  
 W = Week (ex: a = Week 27, z Represents Week 52 and 53)  
 X = Internal Code (ex: U = Monday)

Date Code Key

Year	2016	-	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	6	-	3	4	5	6	7	8	9	0	1	2

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	-60	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-7.7 -6.2	A
	t < 10s T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-10.3 -8.2	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	-55	A
Maximum Continuous Body Diode Forward Current (Note 5)		I <sub>S</sub>	-2.2	A
Avalanche Current, L = 0.1mH		I <sub>AS</sub>	-35.5	A
Avalanche Energy, L = 0.1mH		E <sub>AS</sub>	62.9	mJ

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P <sub>D</sub>	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>θJA</sub>	123	°C/W
	t < 10s		69	
Total Power Dissipation (Note 6)		P <sub>D</sub>	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>θJA</sub>	60	°C/W
	t < 10s		34	
Thermal Resistance, Junction to Case (Note 6)		R <sub>θJC</sub>	6.3	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.  
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	-1	μA	V <sub>DS</sub> = -60V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1	—	-3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	11	25	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -5A
		—	13	33		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iSS</sub>	—	2569	—	pF	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oSS</sub>	—	179	—	pF	
Reverse Transfer Capacitance	C <sub>rSS</sub>	—	143	—	pF	
Gate Resistance	R <sub>g</sub>	—	8	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	26.5	—	nC	V <sub>DS</sub> = -30V, I <sub>D</sub> = -5A
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	53.1	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	7.1	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	12.6	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	6	—	ns	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -30V, R <sub>g</sub> = 3Ω, I <sub>D</sub> = -5A
Turn-On Rise Time	t <sub>R</sub>	—	7.1	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	110	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	62	—	ns	
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	20	—	ns	I <sub>F</sub> = -5A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	14	—	nC	

Notes: 7. Short duration pulse test used to minimize self-heating effect.  
8. Guaranteed by design. Not subject to product testing.

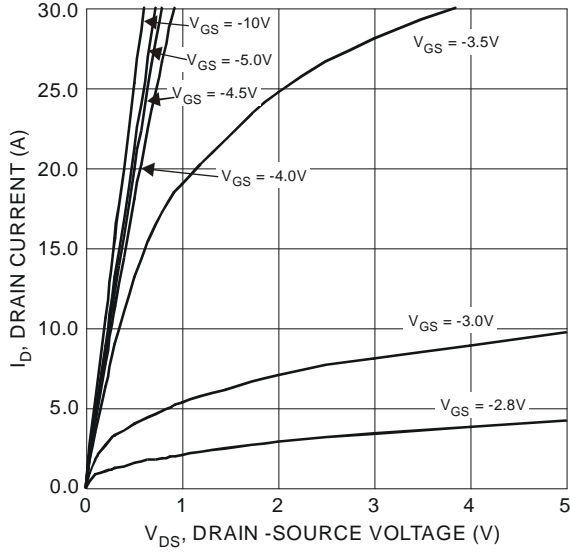


Figure 1 Typical Output Characteristics

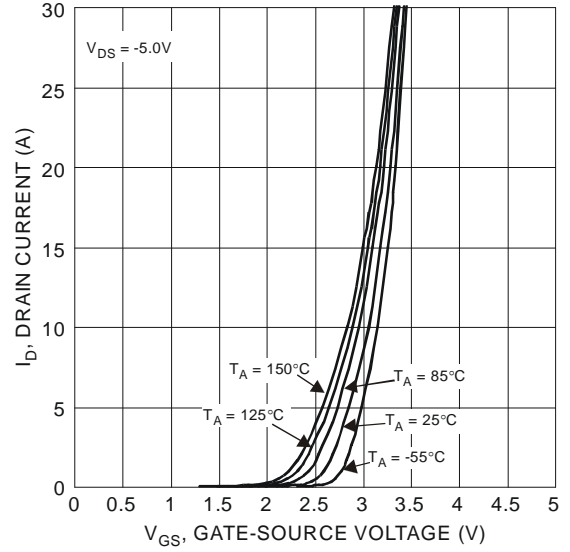


Figure 2 Typical Transfer Characteristics

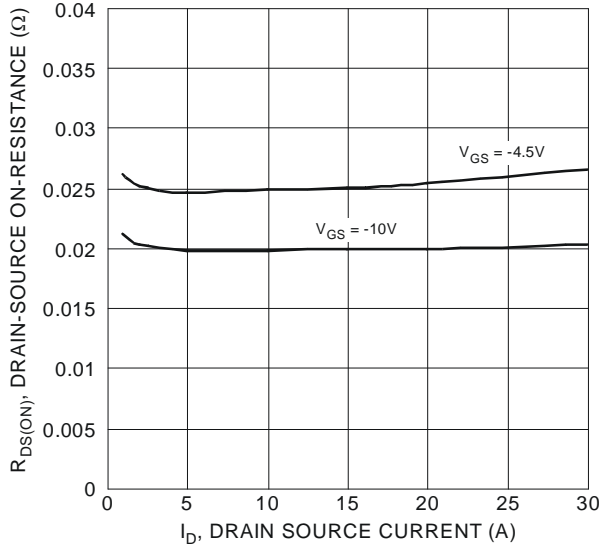


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

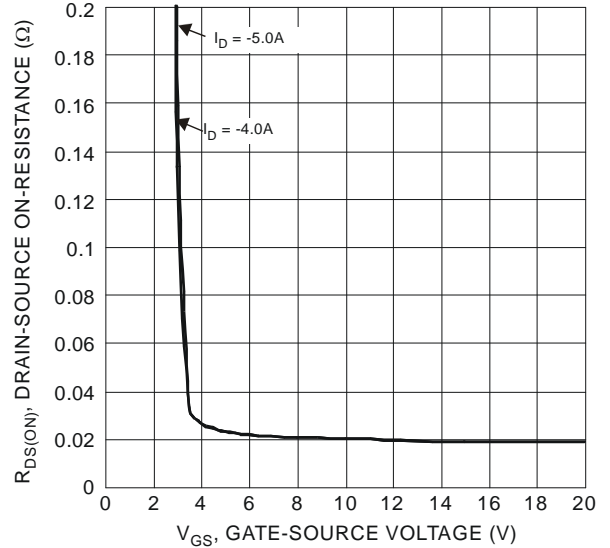


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

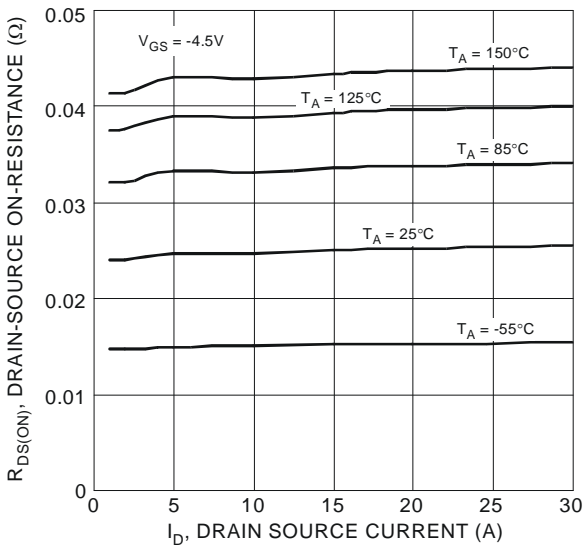


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

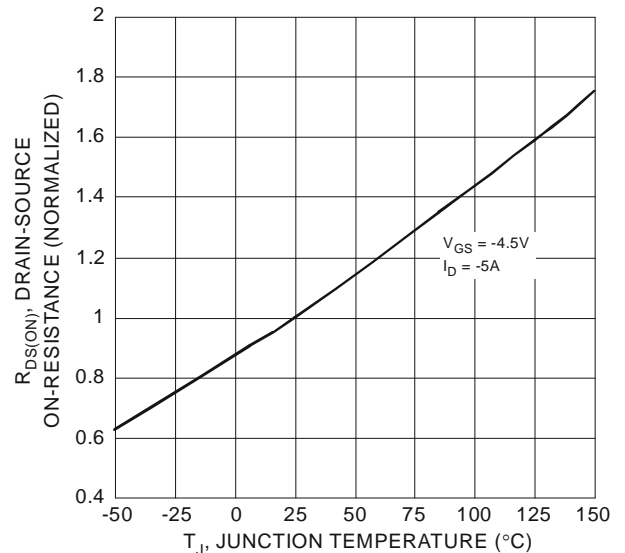


Figure 6 On-Resistance Variation with Temperature

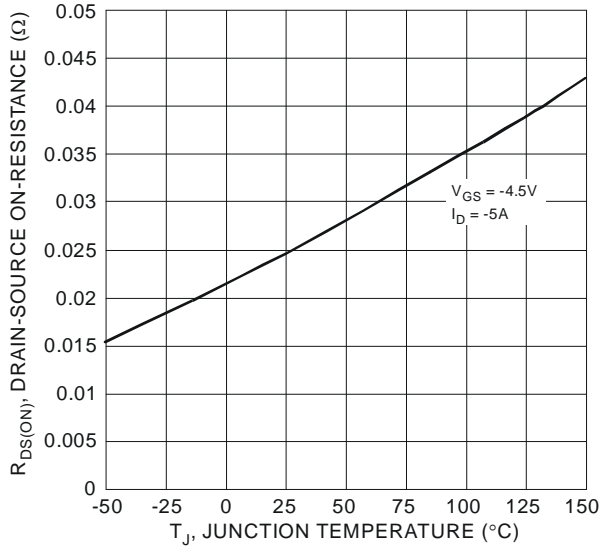


Figure 7 On-Resistance Variation with Temperature

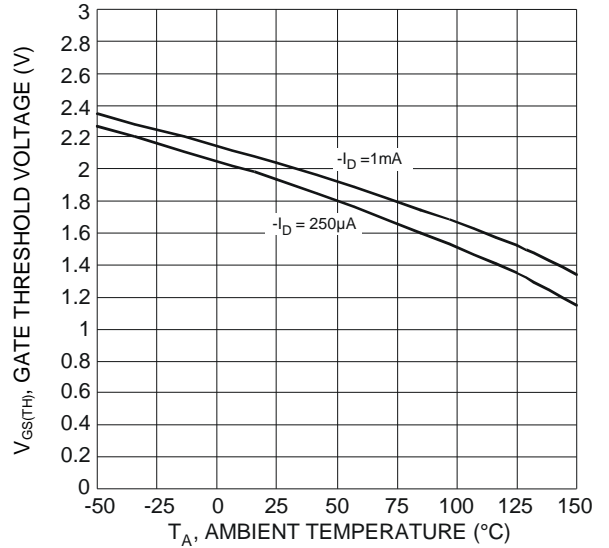


Figure 8 Gate Threshold Variation vs. Ambient Temperature

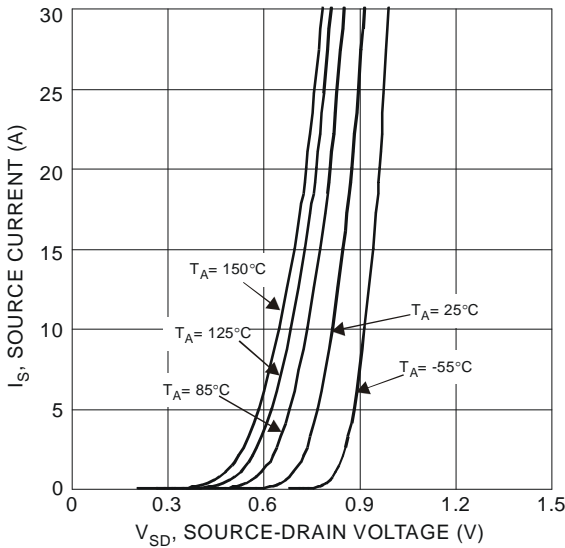


Figure 9 Diode Forward Voltage vs. Current

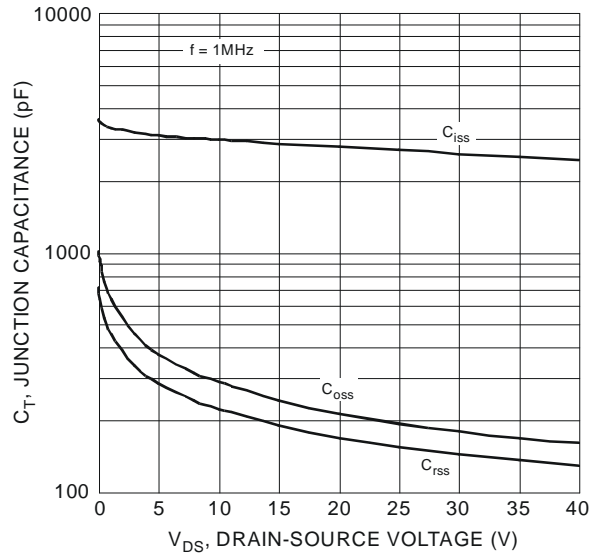


Figure 10 Typical Junction Capacitance

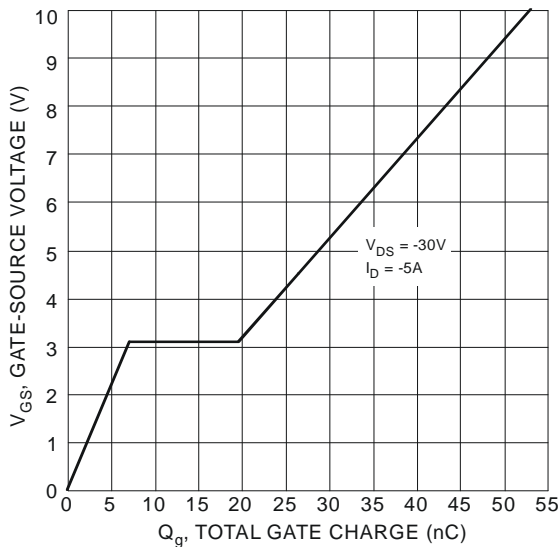


Figure 11 Gate-Charge Characteristics

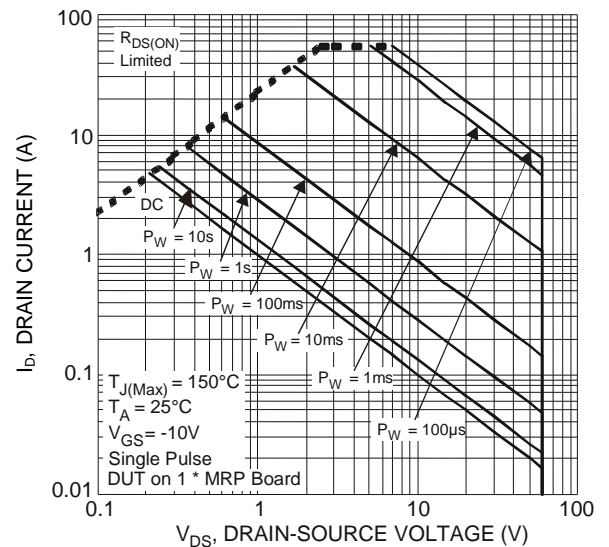
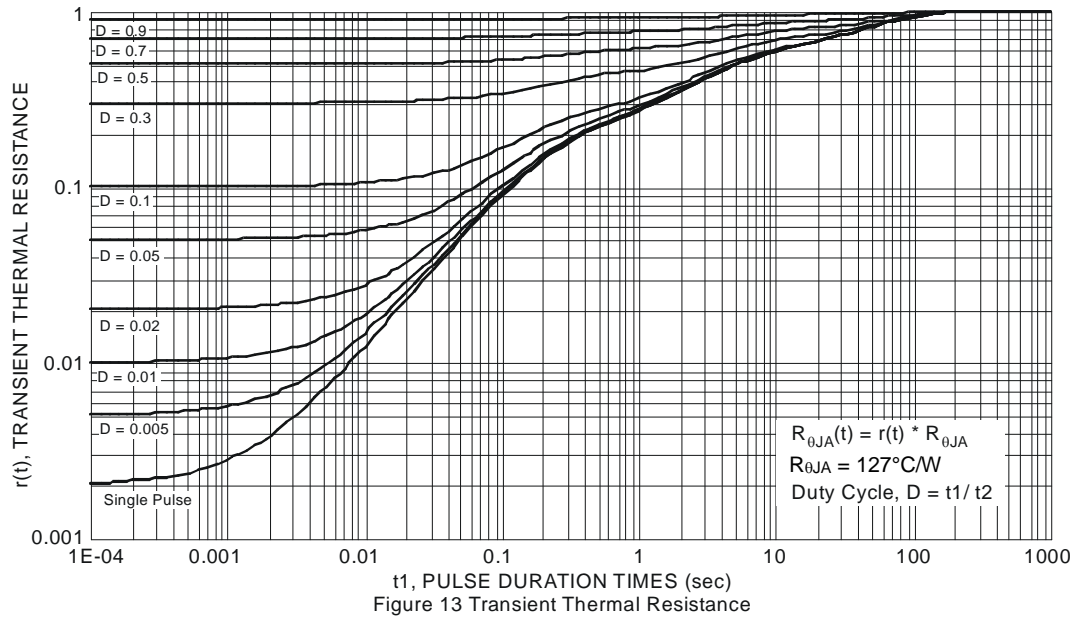


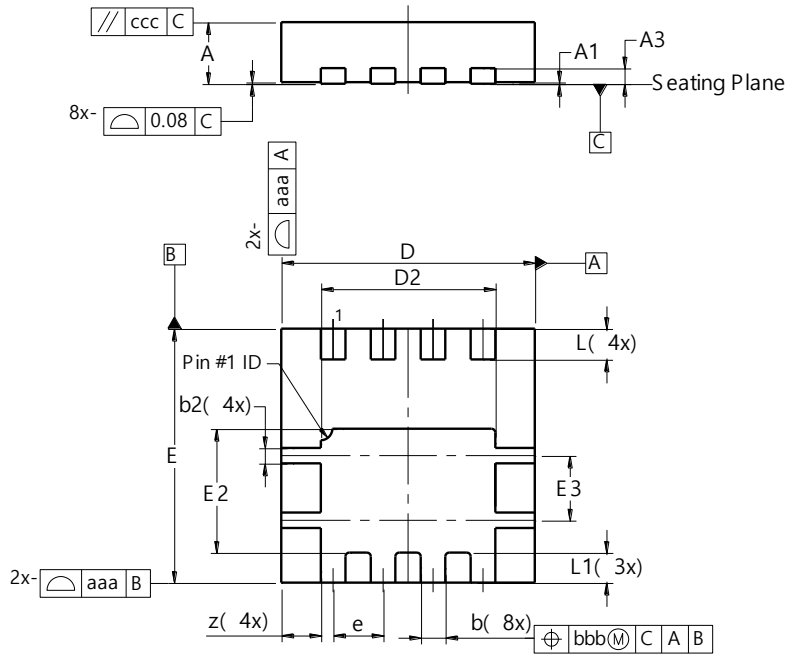
Figure 12 SOA, Safe Operation Area



**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI3333-8**

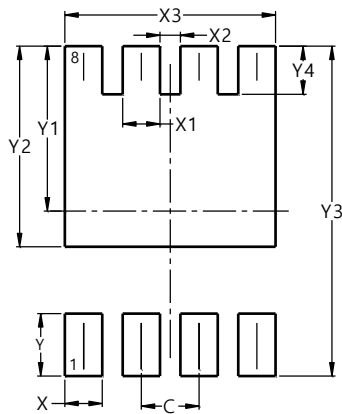


PowerDI3333-8			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	0.02
A3	-	-	0.203
b	0.27	0.37	0.32
b2	-	-	0.20
D	3.25	3.35	3.30
D2	2.22	2.32	2.27
E	3.25	3.35	3.30
E2	1.56	1.66	1.61
E3	0.79	0.89	0.84
e	-	-	0.65
L	0.35	0.45	0.40
L1	-	-	0.39
z	-	-	0.515
aaa	0.25		
bbb	0.10		
ccc	0.10		
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI3333-8**



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540

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