Customer Information Notification | 201603017I

Issue Date: 29-Mar-2016
Effective Date: 27-Jun-2016

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QUALITY

Management Summary
LPC408x/7x data sheet updates for dynamic external memory interface timing.
Change Category

[ ] Wafer Fab Process	[ ] Assembly Process	[ ] Product Marking	[ ] Test Process	[ ] Design
[ ] Wafer Fab Materials	[ ] Assembly Materials	[ ] Mechanical Specification	[ ]Test Equipment	[ ] Errata
[ ] Wafer Fab Location	[ ] Assembly Location	[ ] Packing/Shipping/Labeling	[ ] Test Location	[X] Electrical spec./Test coverage

LPC408x/7x data sheet update to Rev. 3.4.

Information Notification

The data sheet for the LPC408X/7X has been updated to Rev. 3.4.

Tables 16 through 18 have been updated with corrected formulas to calculate timing values.

- Updated Table 16 "Dynamic characteristics: Dynamic external memory interface, read strategy bits (RD bits) = 00".
- Updated Table 17 "Dynamic characteristics: Dynamic external memory interface, read strategy bits (RD bits) = 01".
- Updated Table 18 "Dynamic characteristics: Dynamic external memory interface programmable clock delays (CMDDLY, FBCLKDLY, CLKOUT0DLY and CLKOUT1DLY)".
- Updated Figure 22 "Dynamic external memory interface signal timing".

See data sheet Tables and Revision History for details.

The revised data sheet is available on the NXP website.

There is no change to the product
Why do we issue this Information Notification
For clarification of data sheet parameters.

Identification of Affected Products
Product identification does not change

Impact

Although no changes have been made to the product, customers should evaluate their applications if an external memory interface is used to interface SDRAM.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:
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