201013898 EFR32MG/EFR32FG14 Datasheet Release v1.3

Bulletin Issue Date: 10/13/2020   **Effective Date:**   10/13/2020

Description of Change

Silicon Labs is pleased to announce a new datasheet revision 1.3 for EFR32MG14/EFR32FG14 devices.

Reason for Change

Revision 1.3

• In 1. Feature List, removed Wake on Radio reference.
• In the front page block diagram, updated the lowest energy mode for LETIMER.
• Updated 3.6.4 Low Energy Timer (LETIMER) lowest energy mode.
• In Table 4.2 General Operating Conditions on page 23, Added additional test condition rows (VSCALE2/WS0, VSCALE0/WS1/WS0)
to HFCORECLK frequency specification.
• In, Table 4.38 General-Purpose I/O (GPIO) on page 80, added footnotes to VIL, VIH (footnote 1), and RPUD (footnote 2).
• In Table 4.39 Voltage Monitor (VMON) on page 82, updated test conditions for IVMON.
• In Table 4.42 Digital to Analog Converter (VDAC) on page 88, updated test conditions for IDAC, 200 Hz refresh rate.
• Updated External PA supply connection conditions for SPURHARM_ETSI and SPUROOB_ETSI in section
4.1.10.3 Sub-GHz RF Transmitter characteristics for 868 MHz Band.
• In Table 4.18 Sub-GHz RF Transmitter characteristics for 915 MHz Band on page 50:
• Corrected FCC reference for non-restricted bands in:
• SPURHARM_FCC_20
• SPUROOB_FCC_20
• SPURHARM_FCC_14
• SPUROOB_FCC_14
• Updated typical and maximum specifications for restricted bands (30-88 MHz) in:
• SPUROOB_FCC_20
• SPUROOB_FCC_14
• Added footnote to PSD.
• In 4.1.10.2 Sub-GHz RF Receiver Characteristics for 915 MHz Band, updated typical and maximum specifications for
SPURRX_ARIB, 930-1000 MHz, RBW=100 kHz.
• In 7.3 QFN48 Package Marking, updated feature code.
• In 8.3 QFN32 Package Marking, updated feature code.
• Minor typographical, style, and phrasing changes throughout document.

Product Identification

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Existing Part #
EFR32MG14P632F256GM32-B
EFR32MG14P632F256GM32-BR
EFR32MG14P632F256GM48-B
EFR32MG14P632F256GM48-BR
EFR32MG14P732F256GM32-B
EFR32MG14P732F256GM32-BR
EFR32MG14P732F256GM48-B
EFR32MG14P732F256GM48-BR
EFR32MG14P732F256IM32-B
EFR32MG14P732F256IM32-BR
EFR32MG14P732F256IM48-B
EFR32MG14P732F256IM48-BR
EFR32MG14P733F256GM48-B
EFR32MG14P733F256GM48-BR
EFR32MG14P733F256IM48-B
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EFR32MG14P733F256IM48-BR
EFR32FG14P231F128GM32-B
EFR32FG14P231F128GM32-BR
EFR32FG14P231F128GM48-B
EFR32FG14P231F128GM48-BR
EFR32FG14P231F256GM32-B
EFR32FG14P231F256GM32-BR
EFR32FG14P231F256GM48-B
EFR32FG14P231F256GM48-BR
EFR32FG14P231F256IM32-B
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EFR32FG14P231F256IM48-B
EFR32FG14P231F256IM48-BR
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EFR32FG14P233F128GM48-B
EFR32FG14P233F128GM48-BR
EFR32FG14P233F256GM48-B
EFR32FG14P233F256GM48-BR
EFR32FG14V132F256GM32-B
EFR32FG14V132F256GM32-BR
EFR32FG14V132F256GM48-B
EFR32FG14V132F256GM48-BR
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This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.
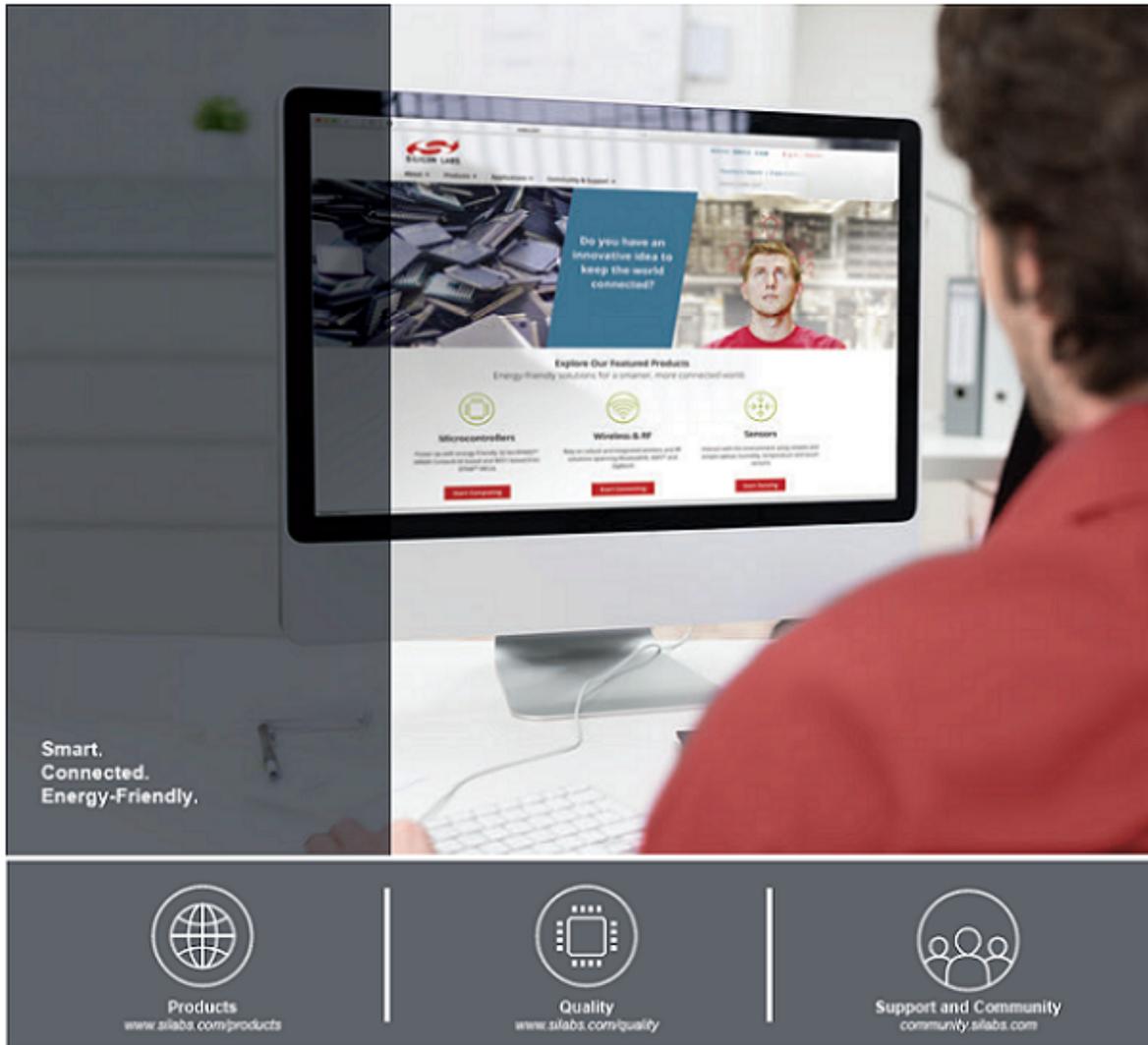
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